

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): T. Sekiguchi, et al.

Filed: March 29, 2004

For: METHOD OF MANUFACTURING SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICES HAVING A MEMORY  
DEVICE WITH REDUCED BIT LINE STRAY CAPACITY AND  
SUCH SEMICONDUCTOR INTEGRATED CIRCUIT DEVICES

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97 AND § 1.98**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 29, 2004

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents cited in connection with prior applications of the above-identified application, that is, Application No. 10/226,290, filed August 23, 2002, and Application No. 08/862,320, filed May 23, 1997.

Since each of No. 10/226,290 and No. 08/862,320 is being relied upon in the above-identified application under 35 USC § 120, copies of the listed documents are not enclosed. See 37 CFR 1.98(d).

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified application. Clearly, requirements of 37 CFR § 1.97(b) are satisfied.

In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR § 1.97 and § 1.98 have been satisfied, in connection with all

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documents on the enclosed List. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 500.35459VV2) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO.	APPLICATION NO.
		500.35459VV2	TBD
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use several sheets if necessary)</i>		<b>APPLICANT</b> T. Sekiguchi, et al.	
		<b>FILING DATE</b> March 29, 2004	<b>GROUP</b> TBD

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
	AA 5,389,560	2/1995	Park			
	AB 5,554,557	9/1996	Koh			
	AC 5,569,948	10/1996	Kim			
	AD 5,677,227	10/1997	Yang, et al.			
	AE 5,710,073	1/1998	Jeng, et al.			
	AF 5,914,279	6/1999	Yang, et al.			
	AG 5,279,989	1/1994	Kim			
	AH 5,296,400	3/1994	Park, et al.			
	AI 5,688,713	11/1997	Linliu, et al.			

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation/ Abstracts	
						Yes	No
	AJ						
	AK						
	AL						

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

AM	Provisional Application No. 60/017,065, Yang, et al., filed April 26, 1996
AN	
AO	
AP	
AQ	
AR	

Examiner	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC